



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q76050

Jaemin LIM, et al.

Appln. No.: 10/694,832

Group Art Unit: 2186

Confirmation No.: 9459

Examiner: Pierre Miche Bataille

Filed: October 29, 2003

For: APPARATUS AND METHOD FOR CONTROLLING EXECUTE-IN-PLACE (XIP) IN
SERIAL FLASH MEMORY, AND FLASH MEMORY CHIP USING THE SAME

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.97 and 1.98

MAIL STOP AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 A & B (modified) form and/or listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

One copy of each of the listed documents is submitted herewith, except for the following: U.S. patents and/or U.S. patent publications; and co-pending non-provisional U.S. applications filed after June 30, 2003.

The present Information Disclosure Statement is being filed: (1) No later than three months from the application's filing date; (2) Before the mailing date of the first Office Action on the merits (whichever is later); or (3) Before the mailing date of the first Office Action after

INFORMATION DISCLOSURE STATEMENT
U.S. Appln. No.: 10/694,832

filing a request for continued examination (RCE) under §1.114, and therefore, no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required. Nevertheless, since a Statement can be made a Statement is submitted herewith

The present Information Disclosure Statement is being filed thirty days or fewer from the communication from a foreign patent office and a Statement Under 37 C.F.R. §1.704(d) is attached.

In compliance with the concise explanation requirement under 37 C.F.R. § 1.98(a)(3) for foreign language documents, Applicant encloses herewith English-language abstracts of the foreign language documents.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

INFORMATION DISCLOSURE STATEMENT

U.S. Appln. No.: 10/694,832

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,



Seok-Won Stuart Lee
Limited Recognition No. L0212

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: October 12, 2006



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q76050

Jaemin LIM, et al.

Appln. No.: 10/694,832

Group Art Unit: 2186

Confirmation No.: 9459

Examiner: Pierre Miche Bataille

Filed: October 29, 2003

For: APPARATUS AND METHOD FOR CONTROLLING EXECUTE-IN-PLACE (XIP) IN
SERIAL FLASH MEMORY, AND FLASH MEMORY CHIP USING THE SAME

STATEMENT UNDER 37 C.F.R. § 1.704(d)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The undersigned hereby states, upon information and belief:

That each item of information contained in the Information Disclosure Statement filed
concurrently herewith was cited in a communication from a foreign patent office in a counterpart
foreign application, and that the communication was not received by any individual designated in
37 C.F.R. § 1.56(c) more than thirty days prior to the filing of said Information Disclosure
Statement.

Respectfully submitted,

Seok-Won Stuart Lee
Limited Recognition No. L0212

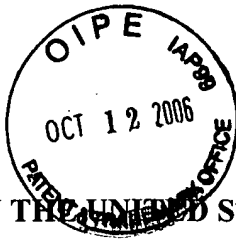
SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: October 12, 2006



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q76050

Jaemin LIM, et al.

Appln. No.: 10/694,832

Group Art Unit: 2186

Confirmation No.: 9459

Examiner: Pierre Miche Bataille

Filed: October 29, 2003

For: APPARATUS AND METHOD FOR CONTROLLING EXECUTE-IN-PLACE (XIP) IN
SERIAL FLASH MEMORY, AND FLASH MEMORY CHIP USING THE SAME

STATEMENT UNDER 37 C.F.R. § 1.97(e)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The undersigned hereby states, upon information and belief:

That each item of information contained in the Information Disclosure Statement filed
concurrently herewith was first cited in any communication from a foreign patent office in a
counterpart foreign application not more than three months prior to the filing of said Information
Disclosure Statement.

Respectfully submitted,

Seok-Won Stuart Lee
Limited Recognition No. L0212

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: October 12, 2006

Substitute for Form 1449 A & B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/694,832
				Confirmation Number	9459
				Filing Date	October 29, 2003
				First Named Inventor	Jaemin LIM
				Art Unit	2186
				Examiner Name	Pierre Miche Bataille
Sheet	1	of	1	Attorney Docket Number	Q76050

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code ² (if known)		
		US			
		US			
		US			
		US			
		US			
		US			

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)			
		JP	2000-100181	A	04-07-2000	Samsung Electronics Co. Ltd.	w/English Abstract
		JP	7-146820	A	06-06-1995	Hitachi Ltd.	w/English Abstract
		JP	7-114500	A	05-02-1995	Matsushita Electric Ind. Co. Ltd.	w/English Abstract
		JP	2001-510612	A	07-31-2001	Intel Corporation	Corresponds to WO 98/29816
		JP	2000-306389	A	11-02-2000	Hitachi Ltd.	w/English Abstract
		JP	6-4399	A	01-14-1994	Hitachi Ltd.	w/English Abstract

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶

Examiner Signature		Date Considered	
--------------------	--	-----------------	--

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²See Kind Codes of USPTO Patent Documents at www.uspto.gov, MPEP 901.04 or follow the hyperlink from the title of the document to the intranet. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to indicate here if English language Translation is attached.